
SERCXI OPEN MANDATE

Senior AI Systems Architect — Chip to Datacentre

A systems architect, not a component owner. This mandate is for the hardware architect who holds the whole stack — silicon, board, rack, row, datacentre — and can defend the performance, power and cost trade-offs in front of customers.

SRX-2616

FILE REFERENCE

Senior

SENIORITY

SG · UAE

LOCATION

35 days

MEDIAN SHORTLIST

SRX-2616 · ENGAGED MANDATE · PREPARED BY SERCXI

THE REMIT

Why this role exists now.

For the first time, system architecture — not chip supply — is the decisive variable in AI compute. Organisations that get the silicon-to-datacentre design right see faster time-to-train, lower energy cost per inference, and infrastructure that scales without being rearchitected two years later. Those that do not spend the difference in power, in retrofits, and in stranded capacity.

That has created a narrow profile. Component specialists are available; architects who can hold mechanical, thermal and power delivery together with board design, rack integration and hyperscale deployment operations are not. Add the presence to sit with customers and supply-chain partners, and the pool thins further — across Singapore, the wider APAC corridor and the Gulf build-out alike.

The client designs and deploys AI compute systems at scale and needs the architect who owns direction for a product scope end to end: from concept and trade-off analysis through large-scale production deployment.

FILE

CORRIDOR

Singapore · UAE (APAC-GCC)

SENIORITY

Senior · 8+ years

DISCIPLINE

Cloud & AI Infrastructure

LANGUAGES

English

WORK MODE

Hybrid — Singapore and/or UAE

EMPLOYMENT

Full-time · Permanent

LISTED

19 Aug 2026 · valid to 30 Nov 2026

WHAT THE ROLE CARRIES

- Drive architecture for complete datacentre systems from the chip outward — performance, power and cost trade-offs across mechanical, thermal and power delivery subsystems at scale.
- Guide product teams with architectural direction, integrating lessons from prior generations with first-principles analysis.
- Work with counterparts across silicon, software, networking and operations to define and deliver cutting-edge products.
- Engage key customers and system development partners across the product development cycle.
- Work with suppliers and procurement to align industry component roadmaps to product and system requirements.
- Monitor competitive solutions and industry trends so architectural decisions stay ahead of the market.
- Take systems from concept through commissioning and large-scale production deployment.

THE PROFILE

What we are looking for.

- Eight or more years in complex product systems, moving from board-level design to full datacentre architecture.
- Deep command of hardware architecture and server build — mechanical, power and thermal design at scale.
- Proficiency designing high-speed multi-layer boards, complex power delivery and signal integrity architectures.
- Hands-on record solving problems that span both software and hardware across large, complex systems.
- Familiarity with datacentre and cloud deployment operations — rack integration, commissioning, hyperscale production.
- Understanding of manufacturing operations and supply chain dynamics, including supplier roadmap alignment.
- Proven ability to lead multi-discipline teams across silicon, software, networking and operations.

THE TERMS

What the client offers.

- Architectural authority over a defined product scope, from concept through production deployment.
- Work at the infrastructure layer of AI compute, alongside specialists of the same grade.
- Direct exposure to hyperscale customers and system development partners.
- Compensation benchmarked to senior systems architecture in the Singapore and UAE markets.
- Hybrid working from Singapore and/or the UAE, with a build-programme travel rhythm.

THE SHAPE OF THE WORK

SCOPE

Chip to datacentre — mechanical, thermal, power, board, rack, row

INTERFACES

Silicon · software · networking · operations · customers · suppliers

ENGAGEMENT

Permanent · hybrid, Singapore and/or UAE

REPORTS TO

Lead Systems Architect / engineering leadership

INDEX READING

The market reading behind the brief.

Current Q3 2026 displacement intensity in the sectors framing this search. Higher intensity means faster leadership turnover and sharper scarcity.

Data Centre	Displaced · 9/9
AI Engineering	Displaced · 9/9
SaaS & Cloud	Displaced · 9/9

Data centre and AI engineering both read at maximum displacement intensity. When compute demand outruns architecture capacity, the scarce profile is not the component specialist — it is the architect who can hold the entire system and say no.

CONFIDENTIALITY

The client is not named in this brief. On an engaged mandate the client's hiring intent is commercially sensitive — particularly in a compute build-out where competitors track hiring signals closely — so the organisation is identified to you under confidence in the first partner conversation.

THE APPROACH

How this mandate runs.

01 · Day 0-1

Approach

You reach out — through the mandate page, the Singapore or Dubai line, or a confidential booking. No application form, no CV portal.

02 · Days 2-7

Partner conversation

Peter Post, the partner who signed this mandate, speaks with you directly. The conversation goes straight to the systems you designed and the trade-offs you defended.

03 · Days 8-21

Client introduction

If the fit holds, Peter introduces you to the hiring client under confidence. You meet engineering leadership and test the architecture scope as it actually stands.

04 · Days 22-35

Decision

The client decides. On a yes, Peter works package, relocation and counter-offer defence with both sides. On a no, you get a direct debrief, not silence.

YOUR PARTNER ON THIS MANDATE

Peter Post

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